

PCB  
LOGO  
Pb-Free Symbol

PCB  
LOGO  
Texas Instruments

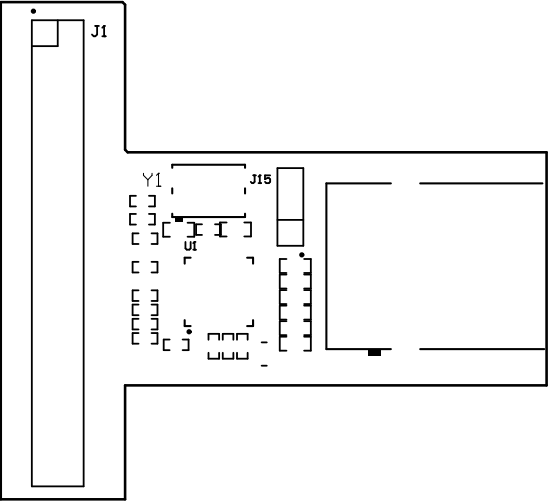
PCB Number: SV012986  
PCB Rev: B

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

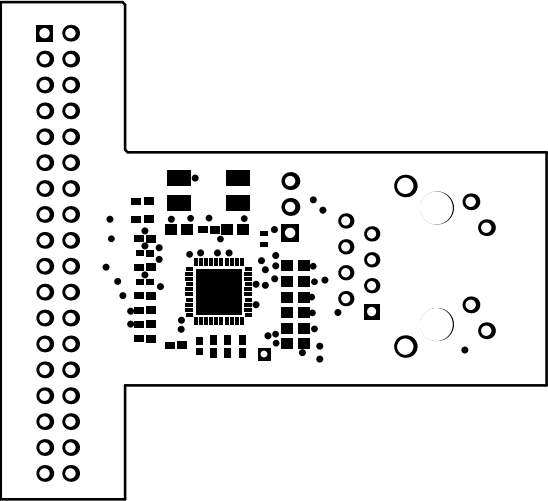
Orderable: DP83848K-MAU-EK	Designed for: Public Release	Mod. Date: 1/20/2015
TID #: J49	Project Title: DP83848K-MAU-EK	
Number: SV012986	Rev: B	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 002	Sheet: 2 of 3
Drawn By:	File: SV012986B SchDoc	Size: C
Engineer: Matt Dillingham	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>



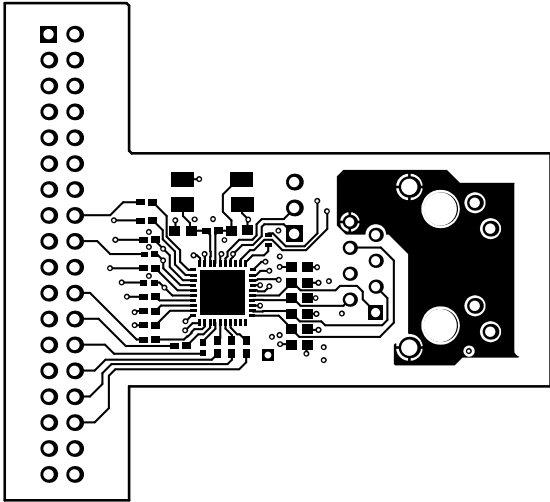
© Texas Instruments 2014



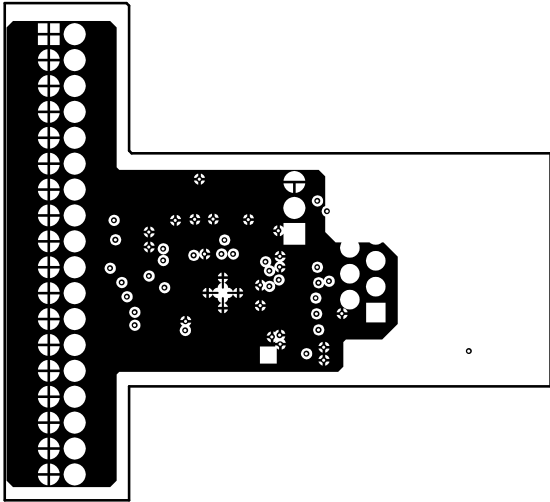
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU012986	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Top Overlay			
PLOT NAME = Top Overlay	GENERATED : 3/9/2016 3:08:59 PM	TEXAS INSTRUMENTS	



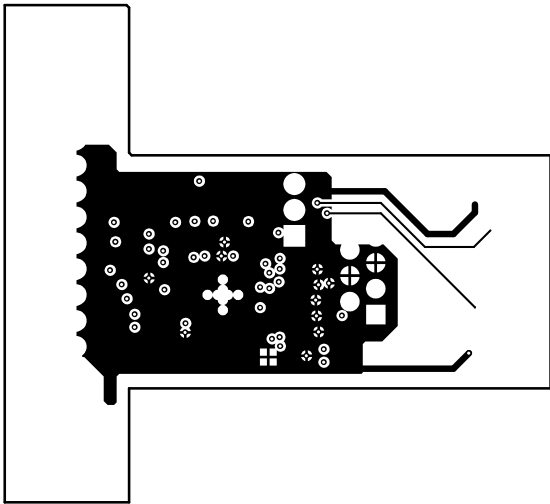
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU012986	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Top Solder			
PLOT NAME = Top Solder Mask	GENERATED : 3/9/2016 3:08:59 PM	TEXAS INSTRUMENTS	



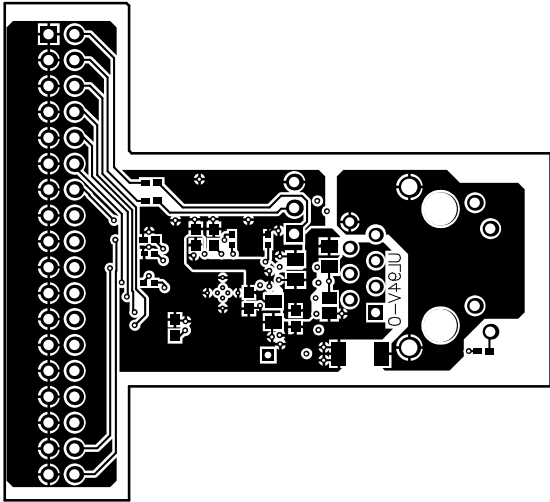
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU012986	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Top Layer			
PLOT NAME = Top Layer	GENERATED : 3/9/2016 3:09:00 PM	TEXAS INSTRUMENTS	



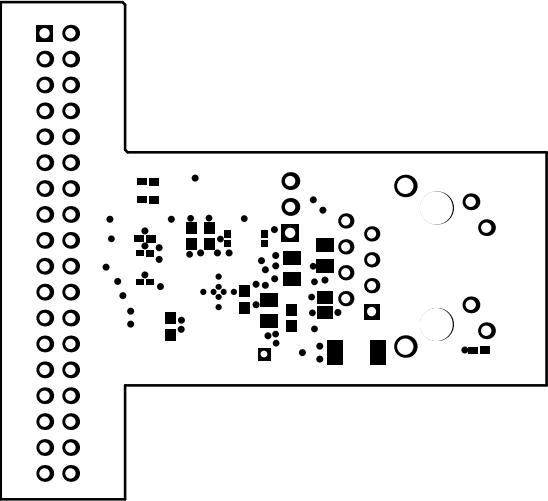
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU012986	REV: B	SUN REV: Not In VersionControl
LAYER NAME = M1 Board Outline			
PLOT NAME = Mid-Layer 1	GENERATED : 3/9/2016 3:09:01 PM	TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU012986	REV: B	SUN REV: Not In VersionControl
LAYER NAME = M1 Board Outline			
PLOT NAME = Mid-Layer 2	GENERATED : 3/9/2016	3:09:02 PM	TEXAS INSTRUMENTS

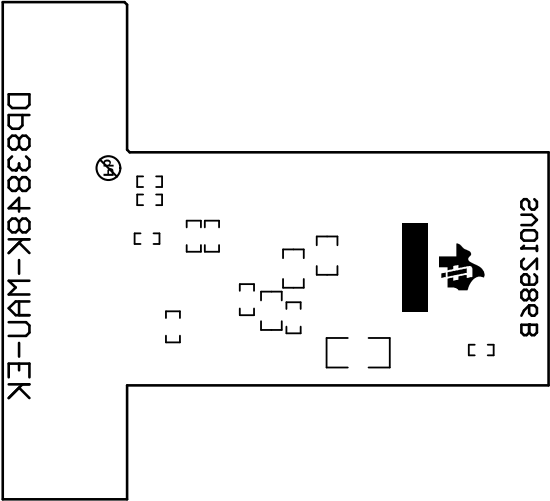


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU012986	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Bottom Layer			
PLOT NAME = Bottom Layer	GENERATED : 3/9/2016 3:09:03 PM	TEXAS INSTRUMENTS	

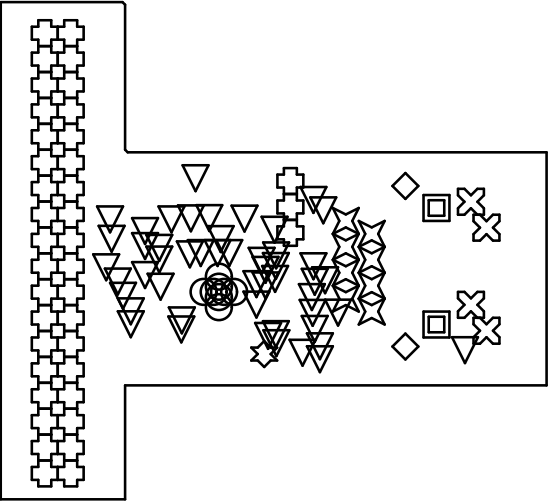


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU012986	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Bottom Solder			
PLOT NAME = Bottom Solder Mask	GENERATED : 3/9/2016 3:09:04 PM	TEXAS INSTRUMENTS	



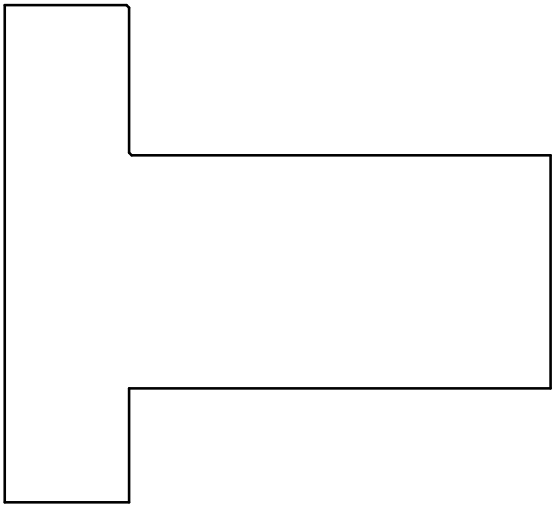


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU012986	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Bottom Overlay			
PLOT NAME = Bottom Overlay	GENERATED : 3/9/2016 3:09:04 PM	TEXAS INSTRUMENTS	



Symbol	Quantity	Finished Hole Size	Plated	Hole Type
■	2	127.95mil (3.250mm)	NPTH	Round
○	8	7.87mil (0.200mm)	PTH	Round
▽	50	12.00mil (0.305mm)	PTH	Round
✱	1	26.00mil (0.660mm)	PTH	Round
✕	8	35.04mil (0.890mm)	PTH	Round
⊕	39	40.00mil (1.016mm)	PTH	Round
⊗	4	40.16mil (1.020mm)	PTH	Round
◇	2	62.99mil (1.600mm)	PTH	Round
	114 Total			

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU012986	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Drill Drawing			
PLOT NAME = Drill Drawing	GENERATED : 3/9/2016 3:09:05 PM	TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU012986	REV: B	SUN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions			
PLOT NAME = Board Dimensions	GENERATED : 3/9/2016 3:09:06 PM	TEXAS INSTRUMENTS	